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	Filing Date		2003-07-29	
	First Named Inventor	Bhushan KANEDKAR		
	Art Unit	3694		
	Examiner Name	BADII, Behrang		
Attorney Docket Number		01935		

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	1	Aristides EFTHYMIU and Jim GARSIDE, "An Adaptive Serial-Parallel CAM Architecture for Low-Power Cache Blocks," Low Power Electronics And Design, 2002. ISLPED '02. Proceedings Of The 2002 International Symposium on Aug. 12-14, 2002, August 12, 2002, pages 136-141, IEEE, Piscataway, NJ, USA.	<input type="checkbox"/>

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